

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#2
J. Coats
6/13/00

In re Application of:
Tai-Chong CHAI et al.

Serial Number:

Filed:

LEAD FRAME FOR AN INTEGRATED
CIRCUIT CHIP (Small Window)

) Patent Group Art Unit:

) Examiner:



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INFORMATION DISCLOSURE STATEMENT

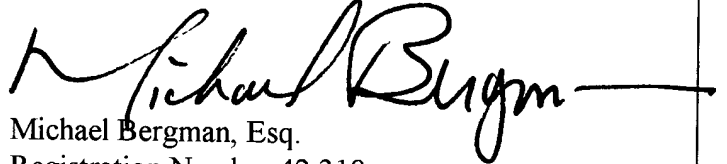
Washington, DC
7 February, 2000

Honorable Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

Pursuant to 37 CFR 1.56, 1.97, and 1.98, submitted herewith for consideration by the Patent and Trademark Office are copies of the English-language documents listed on the enclosed Form 1449. This disclosure is believed to be timely made within three months of the filing date of the above-identified application. The Commissioner is hereby authorized to charge any deficiencies in connection with this paper to Deposit Account No. 23-0575.

Respectfully Submitted


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